

CLAIMS

1. A domain control system comprising:
- 5 a first manager comprising first logic, the first logic, responsive to a first signal, to provide a second signal representative of a first domain and a third signal representative of the first domain and a second domain; and
- a second manager comprising second logic coupled to the first logic, the second logic, responsive to the second and third signals, to operate on a first
- 10 value representative of the first domain and on a second value representative of the second domain, and to provide a fourth signal to control an event defined by the first and second values.
2. A domain control system according to claim 1 wherein the first logic
- 15 adjusts the second signal according to a magnitude of the third signal.
3. A domain control system according to claim 2 wherein the first logic comprises:
- a register to hold the third signal as a plurality of bits; and
- 20 add/subtract logic coupled to the register, the add/subtract logic to increase or decrease the magnitude of the third signal by most significant ones of the plurality of bits.

4. A domain control system according to claim 1 wherein the second logic comprises:

a first register to store the first value;

a second register to store the second value; and

5 add/subtract logic coupled to the first and second registers, the add/subtract logic, responsive to a fifth signal, to adjust the first and second values.

5. A domain control system according to claim 4 wherein the add/subtract logic adjusts the first value by a fixed amount and adjusts the second value by a variable amount.

6. A domain control system according to claim 4 wherein the add/subtract logic adjusts the second value based on the fifth signal and adjusts the first value based on the adjusted second value.

7. A domain control system according to claim 4 wherein the first register has a first capacity set by the first domain, and the second register has a second capacity set by the first domain and the second domain.

8. A domain control system according to claim 7 wherein the first domain is angular, and the first capacity is set by one of a plurality less than one degree.

9. A domain control system according to claim 1 wherein the second value is less than a product of the first value and the third signal.

10. A domain control system according to claim 1 wherein the first domain is angular and the second domain is time.

11. A domain control system comprising:

5 an input to receive a first signal;

an output to provide a second signal, the second signal indicative of a current state of the domain control system;

10 a register coupled to the output, the register to hold the second signal as a plurality of bits, the plurality of bits comprising at least one most significant bit and at least one least significant bit; and

add/subtract logic coupled to the input and the register, the add/subtract logic, responsive to the first signal, to adjust a magnitude of the second signal by the at least one most significant bit.

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